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(74) Agent: **LAW FIRM "GORODISSKY & PARTNERS" LTD.**; EGOROVA, Galina Borisovna, MITS, Alexander Vladimirovich et al., B. Spasskaya Str., 25, Stroenie 3, Moscow, 129010 (RU).

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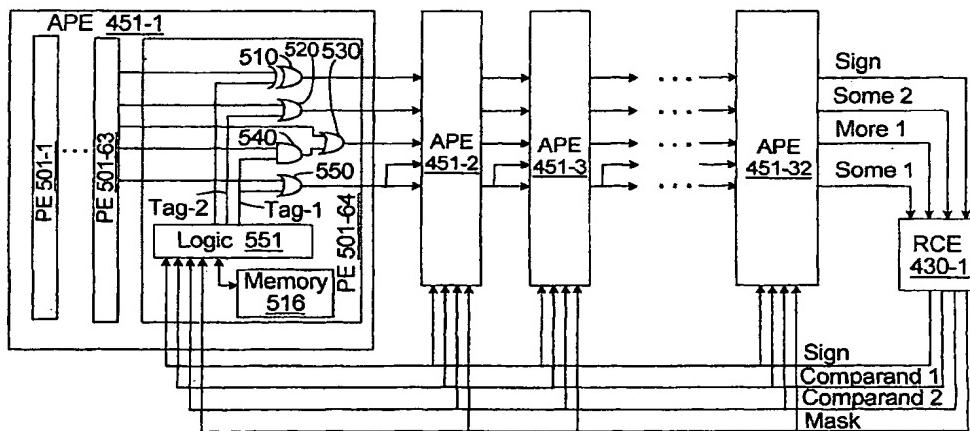
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(71) Applicant (for all designated States except US): **INTEL CORPORATION [US/US]; 2200 Mission College Boulevard, Santa Clara, California 95052 (US).**

(72) Inventors; and

(75) Inventors/Applicants (for US only): **BELOGOLOVY, Andrey Vladimirovich [RU/RU]; Kupchinskaya Str. 30/5, Apt. 33, St.Petersburg, 198226 (RU). EFIMOV, Andrey Gennad'evich [RU/RU]; Varshavskaya Str. 73, Apt. 13, St.Petersburg, 196240 (RU). CHAPYZHENKA, Aliaksei Vladimirovich [BY/RU]; Budapestskaya Str., 101-1-122, St.Petersburg, 192283 (RU).**

(54) Title: MULTI-THESHOLD MESSAGE PASSING DECODING OF LOW DENSITY PARITY CHECK CODES USING THE MIN-SUM PRINCIPLE



(57) Abstract: A message-passing decoder for low-density parity-check codes (LDPC) is provided using a multi-value threshold scheme which is updated throughout the decoding iterations. In an embodiment the check node processing is implemented using the min-sum principle whereby for each corresponding row of the parity check matrix a first and a second minimum value among bit reliability values is determined. Each row of the decoder comprises one or more associative processing elements controlled by a row control element to determine the two minimum values. Each column comprises one or more associative processing elements, an input processing element, and a column control element to determine hard decision bits. The usage of processing elements to construct a decoder may reduce the gate count and decrease the interconnects used to couple the elements.

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